

FACULTY DETAILS

DEPARTMENT


OF


ELECTRONICS

AND


COMMUNICATION


ENGINEERING

Name	Prof. Sampoorna Naidu.B		
Designation	Professor & Vice-Principal		
Date of Birth	14th Jan 1962		
Educational Qualification	B.E., M.E PGDCS, Ph. D		
Work Experience:			
Teaching	20 yrs		
Research	4(incl. teaching)		
Industry	2		
Others	College Administration		
Area of Specializations	Control System, Embedded system		
Subjects teaching at Under Graduate Level:	C & C++, Computer network, Network synthesis & Analysis, CCNA		
	Data Structure, Computer Organization etc		
	Operational Research		
Subjects teaching at Post Graduate Level:	Linear & non linear control engineering		
	Operational Research		
	--		
Research Guidance:	Particulars	Number	
Masters'	--	--	
Ph.D	Embedded system	2	
No. of papers published in (over past three years)	National Journal	International Journal	Conferences
	-	--	National – 2
Projects carried out	Design of Power Wheel Chair Controller using Myo-Signal for a fully disabled persons.		
Patents	--		
Technology Transfers	--		
Research Publications	2		
No. of Books Published with details	3		
Strength	Updating with recent trends in the technology Day never fade !!		
Weakness	Try & Try till succeed		

1.a	Name	SHANTI PRASAD M J		
1.b	Designation	Associate Professor & HOD		
2	Date of Birth	21-6-1952		
3	Educational Qualification	BE., M.TECH., (Ph.D)		
Signature of the Faculty †				
4	Work Experience:	Teaching	25 Years	
		Research	-	
		Industry	-	
		Others	-	
5	Area of Specializations	Mixed-Signal VLSI		
6.a	Subjects teaching at Under Graduate Level:	Field Theory, Electronic Circuits		
		Solid State Devices and Technology		
		VLSI Circuits		
		--		
6.b	Subjects teaching at Post Graduate Level:	RF Communications		
		Digital Design		
		--		
		--		
7.a	Research Guidance:	Particulars	Number	
	Masters'	--	20	
	Ph.D	--	Nil	
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	4
8	Projects carried out	2		
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	-4-		
12	No. of Books Published with details	-2- Field and Waves, Sapna Publishers.		
13	Strength Weakness	Quick decision taking		

1.a	Name	Prof. S Kalyana Raman		 PHOTOGRAPH
1.b	Designation	Professor		
2	Date of Birth	10-06-1942		
3	Educational Qualification	B.SC, DMIT, M.S.(Communication)		
Signature of the Faculty				
4	Work Experience:	Teaching	2 ½ Years	
		Research	34 Years	
		Industry	--	
		Others	--	
5	Area of Specializations	Communication		
6.a	Subjects teaching at Under Graduate Level:	Digital Communication		
		Antenna and Wave Propagation		
		Satellite Communication		
		--		
6.b	Subjects teaching at Post Graduate Level:	Digital Image Processing		
		Digital and Spread spectrum Communication		
		--		
		--		
.a	Research Guidance:	Particulars	Number	
	Masters'	GPS System	01	
		Digital Demodulator	02	
		Advanced audio coder	03	
Ph.D	--	--		
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		10	02	05
8	Projects carried out	Indian Remote Sensing Satellite Projects at ISRO		
9	Patents	--		
10	Technology Transfers	Since worked in Proprietary Projects, Technology Transfer not allowed		
11	Research Publications	12		
12	No. of Books Published with details	Worked as Guest Editor for Journal of Space Craft Technology		
13	Strength	-		

1.a	Name	Prof. C.E. Olaganathan		
1.b	Designation	Associate Professor		
2	Date of Birth	05-09-1941		
3	Educational Qualification	M.Tech		
4	Work Experience:	Teaching	4 Years	
		Research	37 Years	
		Industry	37 Years	
		Others	--	
5	Area of Specializations	Communication, Radar.		
6.a	Subjects teaching at Under Graduate Level:	Computer Communication Network		
		Basic Electronics		
		Mobile Communication		
		Satellite Communication		
6.b	Subjects teaching at Post Graduate Level:	Mobile Communication		
		Advance Digital Communication		
		Computer Communication Network		
		--		
7.a	Research Guidance:	Particulars	Number	
	Masters'	Communication	04	
	Ph.D	--		
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	one
8	Projects carried out	Radar & Communication Projects at DRDO		
9	Patents	--		
10	Technology Transfers	3 Projects Completed With Complete Defense Trails and Handed over to Bharth Electronics for Production		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength			
	Weakness			

1.a	Name	Mr. Ganesh Kumar M.T.		
1.b	Designation	Associate Professor		
2	Date of Birth	20-05-1962		
3	Educational Qualification	M.E.		
				
		PHOTOGRAPH		
4	Work Experience:			
	Teaching	19 Years		
	Research	--		
	Industry	8 Month		
	Others	--		
5	Area of Specializations	Electronics		
6.a	Subjects teaching at Under Graduate Level:	Logic Design		
		VHDL		
		Micro Processor		
6.b	Subjects teaching at Post Graduate Level:	Real Time systems		
		Synthesis and Optimisation of Digital circuits		
		--		
		--		
7.a	Research Guidance:	Particulars		Number
	Masters'	--		01
	Ph.D	--		
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	one
8	Projects carried out	--		
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength	-		

1.a **Name** Mrs Kiran Gupta
 1.b **Designation** Asst Professor
 2 **Date of Birth** 15/04/1969
 3 **Educational Qualification** B.E
 M Tech




4 **Work Experience:** **Signature of the Faculty** †
 08 years
Teaching
Research 1
Industry 6
Others nil
 5 **Area of Specializations** **VLSI Design and Embedded**
 6.a **Subjects teaching at Under Graduate Level:**


6.b **Subjects teaching at Post Graduate Level:** VLSI Technology
 Embedded System Design

7.a	Research Guidance:	Particulars		Number
	Masters'			
	Ph.D			
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences


4

8 **Projects carried out** ISRO ,ITI R&D DSP Group cyber security
 9 **Patents** -
 10 **Technology Transfers** -
 11 **Research Publications** -
 12 **No. of Books Published with details** -
 13 **Strength** **Hard working and regular**

1.a	Name	Ms. K.P. Shashikala		
1.b	Designation	Asst. Professor		
2	Date of Birth	10-02-62		
3	Educational Qualification	B.E. M.Tech.		
Signature of the Faculty				
4	Work Experience:	Teaching	16 Years	
		Research	2 Years in R & D at Murphy India	
		Industry	2 Years in Murphy India	
		Others	--	
5	Area of Specializations	Digital Communication		
6.a	Subjects teaching at Under Graduate Level:	Power Electronics, Microprocessor (8085)		
		Advanced Microprocessor, Digital Communication		
		Digital Electronics, Basic Electronics		
		--		
6.b	Subjects teaching at Post Graduate Level:	--		
		--		
		--		
		--		
7.a	Research Guidance:	Particulars	Number	
	Masters'	--	--	
	Ph.D	--	--	
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	--
8	Projects carried out	BE, Student Project, RF Tracking (M.Tech Project)		
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength	Weakness		
	Strong in Micro Processors and commn related subjects	Not handled mathematical oriented subjects		

1.a	Name	Pushpalatha K. N		
1.b	Designation	Asst. Professor		
2	Date of Birth	01-01-1970		
3	Educational Qualification	M.S.		
				
Signature of the Faculty				†
4	Work Experience:	14 Years		
	Teaching			
	Research			
	Industry			
	Others			
5	Area of Specializations	Electronics and Communication		
6.a	Subjects teaching at Under Graduate Level:	Basic Electronics, Logic Design, Computer Organization, Linear Integrated Circuits		
		TLWG, Information Theory & Coding		
		VLSI Circuits		
6.b	Subjects teaching at Post Graduate Level:			
7.a	Research Guidance:	Particulars		Number
	Masters'	--		--
	Ph.D	--		
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	
8	Projects carried out			
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength			

1.a	Name	Jamuna S		
1.b	Designation	Assistant Professor		
2	Date of Birth	18-5-1972		
3	Educational Qualification	B.E., M.Tech- VLSI Desing		
				
Signature of the Faculty				
4	Work Experience:			
	Teaching	8.7 years		
	Research	-		
	Industry	1.4 years at Reliable Electronics Pvt. Ltd.,		
	Others	-		
5	Area of Specializations	VLSI Design and Embedded Systems		
6.a	Subjects teaching at Under Graduate Level:	Fundamentals of CMOS Design VLSI Circuits, Fundamentals of HDL, Logic Design		
		--		
6.b	Subjects teaching at Post Graduate Level:	Nil		
		--		
		--		
7.a	Research Guidance:	Particulars	Number	
	Masters'		Nil	
	Ph.D	--	Nil	
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	--
8	Projects carried out	FPGA Implementtation of PID controller		
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength	I am optimistic and believe in hard work Hard working and Determination to do		


1.a	Name	Mr. Hanumantharaju M C			
1.b	Designation	Assistant Professor			
2	Date of Birth	20-9-79			
3	Educational Qualification	M.Tech (Ph.D.)			
Signature of the Faculty					
4	Work Experience:	Teaching	6 years		
		Research	5 years		
		Industry	-		
		Others	-		
5	Area of Specializations	Digital Communication and Networking			
6.a	Subjects teaching at Under Graduate Level:	CAD for VLSI, CMOS, VLSI Design, VHDL, Microprocessor, Computer architecture			
		-			
		--			
6.b	Subjects teaching at Post Graduate Level:	Nil			
		-			
		--			
		--			
7.a	Research Guidance:	Particulars		Number	
	Masters'			-	
	Ph.D	--		-	
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences	
		--	--	--	
8	Projects carried out	CMOS Temperature sensor			
9	Patents	--			
10	Technology Transfers	--			
11	Research Publications	--			
12	No. of Books Published with details	--			
13	Strength	1. Digital system Design using VHDL 2. VLSI Circuit Hard Work			
	Weakness	Slow at Work			


1.a	Name	SHAHLA SOHAIL		
1.b	Designation	Sr. LECTURER		
2	Date of Birth	27-7-1974		
3	Educational Qualification	BE., M.TECH., (MBA),PGDM.		
				
Signature of the Faculty †				
4	Work Experience:			
	Teaching	9 Years		
	Research	-		
	Industry	-		
	Others	-		
5	Area of Specializations	COMPUTER AND COMMUNICATION		
6.a	Subjects teaching at Under Graduate Level:	Cryptography, Digital Signal Processing, Digital Communication, Optical Communication, Field Theory, Basic Electronics, Network Analysis and Synthesis, Telecommunication Engineering,		
		--		
6.b	Subjects teaching at Post Graduate Level:	Nil		
		--		
		--		
7.a	Research Guidance:	Particulars	Number	
	Masters'		Nil	
	Ph.D	--	Nil	
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	--
8	Projects carried out	--		
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength	Shy nature.		
	Weakness			


1.a	Name	SATISH S BHAIRANNAWAR		
1.b	Designation	Senior Lecturer		
2	Date of Birth	29-04-1979		
3	Educational Qualification	BE., M.E.		
				
Signature of the Faculty				†
4	Work Experience:	5 Years		
	Teaching			
	Research	-1 year		
	Industry	-		
	Others	-		
5	Area of Specializations	Signal processing on VLSI		
6.a	Subjects teaching at Under Graduate Level:	Field Theory, Electronic Circuits		
		Solid State Devices and Technology		
		VLSI Circuits and VHDL		
		--		
6.b	Subjects teaching at Post Graduate Level:			
		--		
		--		
		--		
7.a	Research Guidance:	Particulars	Number	
	Masters'	--		
	Ph.D	--		
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	
8	Projects carried out			
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications			
12	No. of Books Published with details			
13	Strength	Weaknes		

1.a	Name	Poonam Upamanyu Sharma		
1.b	Designation	Lecturer		
2	Date of Birth	22-01-1985		
3	Educational Qualification	B.E. (Electronics and Communication)		
Signature of the Faculty				†
4	Work Experience:			
	Teaching	ONE YEAR		
	Research	--		
	Industry	---		
	Others	---		
5	Area of Specializations	Electronics and Communication		
6.a	Subjects teaching at Under Graduate Level:			
		Electronics Circuits		
		VLSI Lab		
		--		
6.b	Subjects teaching at Post Graduate Level:			
		--		
		--		
		--		
		--		
7.a	Research Guidance:	Particulars	Number	
	Masters'	--	--	
	Ph.D	--		
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	
8	Projects carried out	--		
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength	Weeknees		
	Teamwork,Patience,Punctuality, Questioning Ability And Urge To Learn.	Emotionals		


1.a	Name	V Anitha		
1.b	Designation	Sr. Lecturer		
2	Date of Birth	01-04-1979		
3	Educational Qualification	M E		
Signature of the Faculty				†
4	Work Experience:	6.1 yrs		
	Teaching	-		
	Research	-		
	Industry	-		
	Others	-		
5	Area of Specializations	Applied Electronics		
6.a	Subjects teaching at Under Graduate Level:	Analog and Digital Communication		
		Computer Networks		
		Basic Electronics		
		High performance networks		
6.b	Subjects teaching at Post Graduate Level:	Embedded Systems		
		Internet Java programming tools		
		Multi media systems		
7.a	Research Guidance:	Particulars	Number	
	Masters'	Sensor Networks, MANET	-	
	Ph.D	-	-	
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		-	-	11
8	Projects carried out	9		
9	Patents	-		
10	Technology Transfers	-		
11	Research Publications	3		
12	No. of Books Published with details	-		

1.a	Name	P.J.Sapna		
1.b	Designation	Lecturer		
2	Date of Birth	30/4/1972		
3	Educational Qualification	BE.,		
4	Work Experience:	Teaching	5 Years	
		Research	-	
		Industry	-	
		Others	-	
5	Area of Specializations	Electronics & Communication		
6.a	Subjects teaching at Under Graduate Level:	Communication Theory, Telephony & Telegraphy, Antennas & Wave Propagation, Intergrated Circuits –I, Network Analysis, Basic Electronics, Logic Design, Analog communication, Computer org.,		
6.b	Subjects teaching at Post Graduate Level:	Nil		
7.a	Research Guidance:	Particulars		Number
	Masters'			Nil
	Ph.D	--		Nil
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	--
8	Projects carried out	--		
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength	Weakness		

1.a	Name	K N Shilpalavanya			
1.b	Designation	Lecturer			
2	Date of Birth	13-08-1983			
3	Educational Qualification	BE M.Tech			
Signature of the Faculty					†
4	Work Experience:	11 months			
	Teaching				
	Research	-			
	Industry	-			
	Others	-			
5	Area of Specializations	Digital electronics and communication			
6.a	Subjects teaching at Under Graduate Level:	Field Theory, Antenna & Propagation, and Satellite comm			
6.b	Subjects teaching at Post Graduate Level:	-			
		-			
		-			
		-			
7.a	Research Guidance:	Particulars		Number	
	Masters'	-		-	
	Ph.D	-		-	
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences	
		-	-	-	
8	Projects carried out	Adaptive data acquisition using ARM.			
9	Patents	-			
10	Technology Transfers	-			
11	Research Publications	-			
12	No. of Books Published with details	-			
13	Strength Hard working and regular	-			
14	Weakness	-			


1.a	Name	T Arun		
1.b	Designation	Lecturer		
2	Date of Birth	20-04-1980		
3	Educational Qualification	BE M.Tech		
Signature of the Faculty				†
4	Work Experience:	Fresher		
	Teaching			
	Research	-		
	Industry	-		
	Others	-		
5	Area of Specializations	VLSI		
6.a	Subjects teaching at Under Graduate Level:	Basic Electronics		
		-		
		-		
		-		
6.b	Subjects teaching at Post Graduate Level:	-		
		-		
		-		
		-		
7.a	Research Guidance:	Particulars		Number
	Masters'	-		0
	Ph.D	-		0
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
8	Projects carried out	Adaptive data acquisition using ARM.		
9	Patents	-		
10	Technology Transfers	-		
11	Research Publications	-		
12	No. of Books Published with details	-		
13	Strength Hard working and regular	-		
14	Weakness	-		

1.a	Name	Mr. Nirmalkumar S Benni			
1.b	Designation	Lecturer			
2	Date of Birth	11-09-1982			
3	Educational Qualification	B.E [E&C] M.Tech [DCN]			
					
Signature of the Faculty †					
4	Work Experience:	9 months			
	Teaching	-			
	Research	-			
	Industry	2 months			
	Others	-			
5	Area of Specializations	Digital Communication and Networking			
6.a	Subjects teaching at Under Graduate Level:				
		Basic Electronics, Control System			
		-			
6.b	Subjects teaching at Post Graduate Level:				
7.a	Research Guidance:	Particulars		Number	
	Masters'				
	Ph.D				
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences	
8	Projects carried out	-			
9	Patents	-			
10	Technology Transfers	-			
11	Research Publications	-			
12	No. of Books Published with details	-			
13	Strength Hard working and regular	-			

1.a	Name	S THENMOZHI			
1.b	Designation	Lecturer			
2	Date of Birth	30/07/1972			
3	Educational Qualification	B.E M.E			
Signature of the Faculty					†
4	Work Experience:	4 years			
	Teaching				
	Research				
	Industry				
	Others				
5	Area of Specializations	APPLIED ELECTRONICS			
6.a	Subjects teaching at Under Graduate Level:	DIGITAL SIGNAL PROCESSING			
		MICROPROCESSOR			
		ELECTRONIC CIRCUITS			
		MICROCONTROLLER			
6.b	Subjects teaching at Post Graduate Level:	DIGITAL FUNDAMENTALS & COMPUTER			
7.a	Research Guidance:	Particulars		Number	
	Masters'			4	
	Ph.D				
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences	
				one	
8	Projects carried out				
9	Patents				
10	Technology Transfers				
11	Research Publications				
12	No. of Books Published with details				
13	Strength Hard working and regular	Research aptitude			


1.a	Name	Kashirya Biradar		
1.b	Designation	Lecture		
2	Date of Birth	22-01-1977		
3	Educational Qualification	B.E M.Tech		
				
Signature of the Faculty				†
4	Work Experience:	4 yrs		
	Teaching	--		
	Research	2 yrs		
	Industry	--		
	Others	--		
5	Area of Specializations	Digital electronics and communication systems		
6.a	Subjects teaching at Under Graduate Level:	Optical fiber Communication, Control system, Basic electronics,		
		Digital Communication		
		-		
		-		
6.b	Subjects teaching at Post Graduate Level:	-		
		-		
		-		
		-		
		-		
7.a	Research Guidance:	Particulars	Number	
	Masters'	-	-	
	Ph.D	-	-	
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		-	-	-
8	Projects carried out	Encore 2 automation		
9	Patents	Cypress semiconductor		
10	Technology Transfers	Embedded systems		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength Hard working and regular	--		


1.a	Name	SHEKAR BABU B		
1.b	Designation	LECTURE		
2	Date of Birth	28-05-1976		
3	Educational Qualification	M.E. APPLIED ELECTRONICS ENGINEERING		
				
Signature of the Faculty †				
4	Work Experience:			
	Teaching			
	Research			
	Industry	WINDOWS 2000 SERVER SUPPORT ENGINEER, HEWLETT PACKARD		
	Others	TRAINER IN DSP AT CITL		
5	Area of Specializations	SPEECH CODEC		
6.a	Subjects teaching at Under Graduate Level:	DIGITAL SIGNAL PROCESSING		
		ANALOG COMMUNICATION		
6.b	Subjects teaching at Post Graduate Level:			
7.a	Research Guidance:	Particulars	Number	
	Masters'			
	Ph.D			
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
8	Projects carried out	LINEAR PREDICTION AND ITU-T G.729A		
9	Patents			
10	Technology Transfers			
11	Research Publications			
12	No. of Books Published with details			
13	Strength Hard working and regular	WANT TO EXPLORE THE NEW TECHNOLOGIES IN SIGNAL PROCESSING AND COMMUNICATION SYSTEMS WITH CADENCE TOOLS		


1.a	Name	Pradeep. M N		
1.b	Designation	Lecture		
2	Date of Birth	30-07-82		
3	Educational Qualification	M.Tech (C.N.E)		
Signature of the Faculty				
4	Work Experience:	Teaching		
		Research		
		Industry	7 months in software industry.	
		Others		
5	Area of Specializations	Computer Network Engineering		
6.a	Subjects teaching at Under Graduate Level:	Basic Electronics.		
6.b	Subjects teaching at Post Graduate Level:			
7.a	Research Guidance:	Particulars	Number	
	Masters'			
	Ph.D			
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
8	Projects carried out	IMPLEMENTATION OF ZIGBEE PROTOCOL FOR WIRELESS COMMUNICATION BETWEEN MEDICAL DEVICES.		
9	Patents			
10	Technology Transfers			
11	Research Publications			
12	No. of Books Published with details			
13	Strength Hard working and regular			


1.a	Name	Deepa N P			
1.b	Designation	Lecturer			
2	Date of Birth	01-06-1978			
3	Educational Qualification	M.Tech Digital Communication			
Signature of the Faculty				†	
4	Work Experience:				
	Teaching				4 years
	Research				
	Industry				9 months
	Others	8 months			
5	Area of Specializations	Digital Communication			
6.a	Subjects teaching at Under Graduate Level:	Electronic Circuits			
		Power Electronics			
		Microprocessor, Advanced Microprocessor			
		Analog Communication, Basic Electronics....			
6.b	Subjects teaching at Post Graduate Level:	-			
		-			
		-			
		-			
7.a	Research Guidance:	Particulars	Number		
	Masters'	-	-		
	Ph.D	-	-		
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences	
		-	-	-	
8	Projects carried out	-			
9	Patents	-			
10	Technology Transfers	-			
11	Research Publications	-			
12	No. of Published Books with details	-			




1.a	Name	Spurthi Adibatti		
1.b	Designation	Lecturer		
2	Date of Birth	22-07-1977		
3	Educational Qualification	M.Tech.		
Signature of the Faculty †				
4	Work Experience: Teaching Research Industry Others	5 years 6 months		
5	Area of Specializations	Digital communication and networking		
6.a	Subjects teaching at Under Graduate Level:	Microcontroller, Microprocessor, Computer organization		
		Operating System		
		VLSI, Instrumentation		
		Communication Systems		
6.b	Subjects teaching at Post Graduate Level:			
7.a	Research Guidance:	Particulars	Number	
	Masters'			
	Ph.D			
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
8	Projects carried out			
9	Patents			
10	Technology Transfers			
11	Research Publications			
12	No. of Books Published with details			

1.a	Name	Sneha Latha S Gurav		
1.b	Designation	Lecturer		
2	Date of Birth	08-06-1975		
3	Educational Qualification	B.E, M.Tech		
Signature of the Faculty				†
4	Work Experience: Teaching Research Industry Others	6 YRS		
		2 yrs		
5	Area of Specializations	VLSI & Embedded systems		
6.a	Subjects teaching at Under Graduate Level:	Basic Electronics, Control System, VHDL, HDL,		
		Electronics Circuits, CMOS Design, CCM		
6.b	Subjects teaching at Post Graduate Level:	-		
7.a	Research Guidance:	Particulars	Number	
	Masters'	-	-	
	Ph.D			
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		-	-	-
8	Projects carried out	Defiance Research Organization		
9	Patents	-		
10	Technology Transfers	-		
11	Research Publications	-		
12	No. of Published Books with details	-		

1.a	Name	T. Sanjivi Nathan		
1.b	Designation	Lecturer		
2	Date of Birth	02-04-1984		
3	Educational Qualification	B.E		
				
4	Work Experience:	Fresher		
	Teaching	1 ½ Years		
	Research			
	Industry			
	Others			
5	Area of Specializations	VLSI		
6.a	Subjects teaching at Under Graduate Level:	Basic Electronics		
		Digital Systems Design using VHDL		
		--		
		--		
6.b	Subjects teaching at Post Graduate Level:	--		
		--		
		--		
		--		
7.a	Research Guidance:	Particulars		Number
	Masters'	--		--
	Ph.D	--		
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	
8	Projects carried out	--		
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength: Hard work, Quick Learner cum Achiever, Optimist.	Weakness: -----		

1.a	Name	Mr. Sandeep Kumar S			
1.b	Designation	Lecturer			
2	Date of Birth	06 - 9 - 1984			
3	Educational Qualification	B.E. (Electronics and Communication) M.Tech (VLSI Design and Embedded Systems)			
4	Work Experience:				
	Teaching	-			
	Research	-			
	Industry	-			
	Others	-			
5	Area of Specializations	VLSI			
6.a	Subjects teaching at Under Graduate Level:	-			
6.b	Subjects teaching at Post Graduate Level:	-			
7.a	Research Guidance:	Particulars		Number	
	Masters'	-		-	
	Ph. D	-		-	
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences	
			1	1	
8	Projects carried out	-			
9	Patents	-			
10	Technology Transfers	-			
11	Research Publications				
12	No. of Books Published with details	-			
13	Strength				
	Weakness				

1.a	Name	Sampath Kumar K B			
1.b	Designation	Lecturer			
2	Date of Birth	8/6/83			
3	Educational Qualification	B.E., M.Tech			
4	Work Experience:				
	Teaching	-			
	Research	-			
	Industry	Intel Corporation, Bangalore – 1 year			
	Others				
5	Area of Specializations	CMOS VLSI Design Circuit			
6.a	Subjects teaching at Under Graduate Level:	-			
6.b	Subjects teaching at Post Graduate Level:	-			
7.a	Research Guidance:	Particulars	Number		
	Masters'	-	14		
	Ph. D	-	-		
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences	
		-	-	-	
8	Projects carried out	Intel Corporation, Bangalore			
9	Patents	-			
10	Technology Transfers	-			
11	Research Publications	-			
12	No. of Books Published with details	-			
13	Strength	Technical			
	Weakness				

1.a	Name	Pushpamala S		
1.b	Designation	Lecturer		
2	Date of Birth	3-11-83		
3	Educational Qualification	B.E. – E&C		
4	Work Experience: Teaching Research Industry Others	-		
		-		
		-		
		Instructor – 5 years		
5	Area of Specializations	-		
6.a	Subjects teaching at Under Graduate Level:	-		
6.b	Subjects teaching at Post Graduate Level:	-		
7.a	Research Guidance:	Particulars	Number	
	Masters'	-	-	
	Ph. D	-	-	
7. b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		-	-	-
8	Projects carried out	-		
9	Patents	-		
10	Technology Transfers	-		
11	Research Publications	-		
12	No. of Books Published with details	-		
13	Strength	Technical		
	Weakness			

1.a	Name	Mahesh Kumar N.		
1.b	Designation	Lecturer		
2	Date of Birth			
3	Educational Qualification	BE		
		Signature of the Faculty		
4	Work Experience:	1.5 yr		
	Teaching			
	Research			
	Industry			
	Others	5 Years :(Instructor)		
5	Area of Specializations	VLSI Design		
6.a	Subjects teaching at Under Graduate Level:			
		--		
		Antenna & Wave Propagation		
6.b	Subjects teaching at Post Graduate Level:	--		
		--		
		--		
		--		
7.a	Research Guidance:	Particulars		Number
	Masters'	--		--
	Ph.D	--		
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		--	--	One
8	Projects carried out			
9	Patents	--		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	--		
13	Strength	--		

