


**FACULTY DETAILS**


**DEPARTMENT**


**OF**

**TELECOMMUNICATION  
ENGINEERING**

1.a	Name	PRAHLAD T. KULKARNI		
1.b	Designation	Professor & HOD		
2	Date of Birth	11 – 08 – 1955		
3	Educational Qualification	B.E. (Electronics & Comm.), M.Tech. (Electrical & Elns. Engg.) Ph.D. (Electrical & Elns. Engg.)		
4	<b>Work Experience:</b>	<p>“Lecturer” in Gogte Institute of Tech, Belgaum for 1 yr. 5 Months  “Asst. Prof.” in PDA College of Engg., Gulbarga, for 17 yrs.  “Prof. &amp; HOD” in SDM College of Engg. &amp; Tech. for 3 yrs. 4 Months  “Visiting Prof.” in Scoula Superiori Sant Anna, Italy for 2 months  “Principal” in Pune Institute of Computer Tech. for 4 months  “Visiting Prof.” in S. Korea for 6 months  “Prof.” in PES Institute of Technology for 2 yrs.  “Principal” in DSCE for 2 yrs.</p>		
	<b>Teaching</b>			
	<b>Research</b>			
	<b>Industry Others</b>			
		12 yrs. ( Including Teaching)		
		Two Yrs.		
		-		
5	<b>Area of Specializations</b>	Light Wave Networks, Wireless & Satellite Comm.		
6.a	<b>Subjects teaching at Under Graduate Level:</b>	All subject related to Communication & Network		
6.b	<b>Subjects teaching at Post Graduate Level:</b>	All subject related to Communication & Network		
7.a	<b>Research Guidance:</b>	<b>Particulars</b>	<b>Number</b>	
	<b>Masters’</b>	-	15	
	<b>Ph. D</b>	-	02	
7. b	<b>No. of papers published in (over past three years)</b>	<b>National Journal</b>	<b>International Journal</b>	<b>Conferences</b>
		-	07	22
8	<b>Projects carried out</b>	Associated with 4 International projects & National projects		
9	<b>Patents</b>	1 ( in group)		
10	<b>Technology Transfers</b>	-		
11	<b>Research Publications</b>	29 Research papers mainly in international Journals & Conference, 1 White paper on Optical Networks.		
12	<b>No. of Books Published with details</b>	-		
13	<b>Strength</b>			
	<b>Weakness</b>			

1.a	Name	Dr. Srinivasaiah H C		
1.b	Designation	Professor		
2	Date of Birth	16-4-64		
3	Educational Qualification	BE. ME, Ph.D.		
4	Work experience: Teaching	10+ years		
	Research	5 years		
	Industry	3+ years		
	Others	Industrial Training		
5	Area of Specializations	Microtronics / VLSI		
6.a.	Subjects teaching at Under Graduate Level:	VLSI Circuits , CMOS Analog mixed signal circuits, VLSI Technology		
6.b	Subjects teaching at Post Graduate Level:	VLSI System for Architecture		
		Low Power VLSI		
		VLSI Technology / testing verification		
		--		
7.a	Research Guidance:	Particulars	Number	
	Masters'	--	--	
	Ph.D	Guiding under VTU	2	
7.b	No. of papers published in (over past three years)	National Journal	International Journal	Conferences
		----	3	7
8	Projects carried out	AFC for MT1, Image debugging		
9	Patents	---		
10	Technology Transfers	--		
11	Research Publications	--		
12	No. of Books Published with details	Underway Text book "Semiconductor process variability is underway"		
13	Strength Weakness	Hard working		


<b>1.a</b>	<b>Name</b>	K.T Srirangaraj			
<b>1.b</b>	<b>Designation</b>	Professor			
<b>2</b>	<b>Date of Birth</b>	28.02.1955			
<b>3</b>	<b>Educational Qualification</b>	B.Sc (Hons), M.E. ADIM			
<b>Signature of the Faculty</b>					
<b>4</b>	<b>Work experience:</b>				
	<b>Teaching</b>	13 .5 years			
	<b>Research</b>	2 years			
	<b>Industry</b>	3 years			
	<b>Others</b>	14 years			
<b>5</b>	<b>Area of Specializations</b>	Wireless Communication, Telecom Networking			
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	Operations Research			
		Mobile Communication			
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	Wireless & ATM Networks			
		RF & Microwave Devices			
<b>7.a</b>	<b>Research Guidance:</b>				
	<b>Masters'</b>				
	<b>Ph.D</b>				
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences	
<b>8</b>	<b>Projects carried out</b>	Campus Networking; 2.5 g adoptions in Mobile communication			
<b>9</b>	<b>Patents</b>				
<b>10</b>	<b>Technology Transfers</b>				
<b>11</b>	<b>Research Publications</b>				
<b>12</b>	<b>No. of Books Published with details</b>				
<b>13</b>	<b>Strength</b>	Communication Teaching Methodology			
	<b>Weakness</b>	Rigidity			

<b>1.a</b>	<b>Name</b>	Mrs. Manjula Devi T.H	
<b>1.b</b>	<b>Designation</b>	Asst. Professor	
<b>2</b>	<b>Date of Birth</b>	21.06.1965	
<b>3</b>	<b>Educational Qualification</b>	M. E	
<b>Signature of the Faculty</b>			

<b>4</b>	<b>Work experience:</b>  <b>Teaching</b> <b>Research</b> <b>Industry</b> <b>Others</b>	16 years		
		--		
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		--		
<b>5</b>	<b>Area of Specializations</b>	Electronics & Communication		
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	Signals & systems, Analog Communication,		
		Electronic Circuits, Control Systems,		
		Information Theory & Coding,		
		Optical Fiber Communication		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	--		
		--		
		--		
		--		
<b>7.a</b>	<b>Research Guidance:</b>	--		
	<b>Masters'</b>	--		
	<b>Ph.D</b>	--		
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
		--	--	01
<b>8</b>	<b>Projects carried out</b>	--		
<b>9</b>	<b>Patents</b>	---		
<b>10</b>	<b>Technology Transfers</b>	--		
<b>11</b>	<b>Research Publications</b>	--		
<b>12</b>	<b>No. of Books Published with details</b>	--		
<b>13</b>	<b>Strength</b>	Can handle any mathematical subjects		
	<b>Weakness</b>	Very sensitive		

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**Bangalore – 560078**


<b>1.a</b>	<b>Name</b>	Mrs. Madhuri S. Kulkarni	
<b>1.b</b>	<b>Designation</b>	Asst. Professor	
<b>2</b>	<b>Date of Birth</b>	01.05.1970	
<b>3</b>	<b>Educational Qualification</b>	M.E	
<b>Signature of the Faculty</b>			

<b>4</b>	<b>Work experience:</b>			
	Teaching	10 years		
	Research	--		
	Industry	--		
	Others	--		
<b>5</b>	<b>Area of Specializations</b>	Power Electronics		
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	Logic Design, Network Analysis, Control System,		
		Operating System, Operation Research & Management		
		Linear Integrated Circuit Theory		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	Network Protocol Design		
<b>7.a</b>	<b>Research Guidance:</b>	--		
	Masters'	--		
	Ph.D	--		
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
		---	--	--
<b>8</b>	<b>Projects carried out</b>	µP based real time monitoring of power TFMR modified series resonant converter		
<b>9</b>	<b>Patents</b>	--		
<b>10</b>	<b>Technology Transfers</b>	--		
<b>11</b>	<b>Research Publications</b>	--		
<b>12</b>	<b>No. of Books Published with details</b>	--		
<b>13</b>	<b>Strength</b>	Good communication skills Good student feedback		
	<b>Weakness</b>	Lack of hands on exp.		

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REF VII – INDIVIDUAL FACULTY PROFILE

<b>1.a</b>	<b>Name</b>	Mrs. M. Jayalalitha		
<b>1.b</b>	<b>Designation</b>	Asst. Professor		
<b>2</b>	<b>Date of Birth</b>	15.06.1968		
<b>3</b>	<b>Educational Qualification</b>	M. Tech		
<b>Signature of the Faculty</b>				
<b>4</b>	<b>Work experience:</b>			
	<b>Teaching</b>	10 years		
	<b>Research</b>	--		
	<b>Industry</b>	3 years		
	<b>Others</b>	--		
<b>5</b>	<b>Area of Specializations</b>	Digital Systems & Communication Engg.		
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	Microwave Communication		
		Computer Communication & Networks		
		Telecommunication & Switching		
		Microprocessors		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	Error Control Coding		
		Optical Communication & Networking		
<b>7.a</b>	<b>Research Guidance:</b>	--		
	<b>Masters'</b>	--		
	<b>Ph.D</b>	--		
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
		--	--	--
<b>8</b>	<b>Projects carried out</b>	--		
<b>9</b>	<b>Patents</b>	--		
<b>10</b>	<b>Technology Transfers</b>	--		
<b>11</b>	<b>Research Publications</b>	--		
<b>12</b>	<b>No. of Books Published with details</b>	--		
<b>13</b>	<b>Strength</b>	Good Communication skills Good in logical networking related subjects		
	<b>Weakness</b>	Lack of practical information		

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REF VII – INDIVIDUAL FACULTY PROFILE


<b>1.a</b>	<b>Name</b>	Mr. H.C. Sateesh Kumar		
<b>1.b</b>	<b>Designation</b>	Asst. Professor		
<b>2</b>	<b>Date of Birth</b>	20.04.1968		
<b>3</b>	<b>Educational Qualification</b>	M. Tech		
<b>Signature of the Faculty</b>				
<b>4</b>	<b>Work experience:</b>	<b>Teaching</b>	12 years	
		<b>Research</b>	--	
		<b>Industry</b>	--	
		<b>Others</b>	--	
<b>5</b>	<b>Area of Specializations</b>	Biomedical Instrumentation		
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	Analog Communication, Digital Communication,		
		Signals & Systems, Control Systems, Basic Electronics,		
		Analog Electronic Circuits		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	Advanced Digital Communication		
<b>7.a</b>	<b>Research Guidance:</b>		--	--
		<b>Masters'</b>	--	--
		<b>Ph.D</b>	--	--
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
		--	--	--
<b>8</b>	<b>Projects carried out</b>	--		
<b>9</b>	<b>Patents</b>	---		
<b>10</b>	<b>Technology Transfers</b>	--		
<b>11</b>	<b>Research Publications</b>	--		
<b>12</b>	<b>No. of Books Published with details</b>	--		
<b>13</b>	<b>Strength</b>	Good teacher of communication & related subjects		
	<b>Weakness</b>	Do not like processor based subjects.		

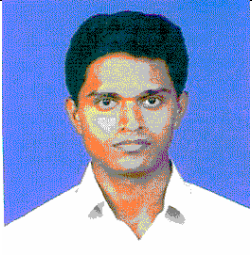
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REF VII – INDIVIDUAL FACULTY PROFILE

<b>1.a</b>	<b>Name</b>	Mrs. Rajeswari .P		
<b>1.b</b>	<b>Designation</b>	Senior Lecturer		
<b>2</b>	<b>Date of Birth</b>	07.05.1976		
<b>3</b>	<b>Educational Qualification</b>	M.E		
<b>Signature of the Faculty</b>				
<b>4</b>	<b>Work experience:</b>			
	Teaching	9 yrs		
	Research	--		
	Industry	--		
	Others	--		
<b>5</b>	<b>Area of Specializations</b>	VLSI Design		
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	Linear Integrated Circuits		
		Electron Devices, Electronic circuits,		
		Microprocessors & its application		
		Digital System Design		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	ASIC, CMOS, VLSI Design, VHDL		
<b>7.a</b>	<b>Research Guidance:</b>			
	Masters'			
	Ph.D			
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
		--	--	02
<b>8</b>	<b>Projects carried out</b>	--		
<b>9</b>	<b>Patents</b>	--		
<b>10</b>	<b>Technology Transfers</b>	--		
<b>11</b>	<b>Research Publications</b>	--		
<b>12</b>	<b>No. of Books Published with details</b>	--		
<b>13</b>	<b>Strength</b>			
	<b>Weakness</b>			


1.a	<b>Name</b>	PRITI S. PAVALE		
1.b	<b>Designation</b>	Sr. Lecturer		
2	<b>Date of Birth</b>	14 – 05 – 1977		
3	<b>Educational Qualification</b>	B.E. (Electronics & Comm.), M.Tech.(Digital Comm. & Networking )		
4	<b>Work Experience: Teaching</b>	“Lecturer” in Gogte Institute of Technology, Belgaum for 4 yrs. 11 months “Lecturer” in CMR Institute of Technology, for 4 yrs. “Senior Lecturer” in Sapthagiri College of Engg., for 4 months		
	<b>Research</b>	-		
	<b>Industry</b>	-		
	<b>Others</b>	-		
5	<b>Area of Specializations</b>	Digital Comm. & Networking		
6.a	<b>Subjects teaching at Under Graduate Level:</b>	Basic Electronics Analog & Digital Communication, ANM, Electronics		
6.b	<b>Subjects teaching at Post Graduate Level:</b>	-		
7.a	<b>Research Guidance:</b>	<b>Particulars</b>	<b>Number</b>	
	<b>Masters’</b>	-	-	
	<b>Ph. D</b>	-	-	
7. b	<b>No. of papers published in (over past three years)</b>	<b>National Journal</b>	<b>International Journal</b>	<b>Conferences</b>
		-	-	-
8	<b>Projects carried out</b>	1. Digital receiver using system view at ISRO 2. Recognition of sensitive parameter using ANM		
9	<b>Patents</b>	-		
10	<b>Technology Transfers</b>	-		
11	<b>Research Publications</b>			
12	<b>No. of Books Published with details</b>	-		
13	<b>Strength</b>	Fundamental concepts & communication skills		
	<b>Weakness</b>			

<b>1.a</b>	<b>Name</b>	Mr. S. Saravana Kumar .S	
<b>1.b</b>	<b>Designation</b>	Lecturer	
<b>2</b>	<b>Date of Birth</b>	13.07.1980	
<b>3</b>	<b>Educational Qualification</b>	M. Tech (VLSI Design)	
<b>Signature of the Faculty</b>			

<b>4</b>	<b>Work experience:</b>	<b>Teaching</b>	2 years		
		<b>Research</b>	--		
		<b>Industry</b>	8 months		
		<b>Others</b>	--		
<b>5</b>	<b>Area of Specializations</b>	VLSI Design (CMOS VLSI Design)			
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	Solid State devices and technology			
		Embedded Systems, CAD for VLSI,			
		Digital System Design using VHDL, Basic electronics			
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	--			
<b>7.a</b>	<b>Research Guidance:</b>	For B.E. Academic projects			
		Masters'		--	
		Ph.D		--	
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences	
		--	--	--	
<b>8</b>	<b>Projects carried out</b>	USB device controller-FPGA Implementation			
<b>9</b>	<b>Patents</b>	--			
<b>10</b>	<b>Technology Transfers</b>	--			
<b>11</b>	<b>Research Publications</b>	--			
<b>12</b>	<b>No. of Books Published with details</b>	--			
<b>13</b>	<b>Strength</b>	Good in logical & analytical reasoning			
	<b>Weakness</b>	Lack of industrial exposure			

1.a	Name	Mr. Mahesh.D	
1.b	Designation	Lecturer	
2	Date of Birth	01.05.1983	
3	Educational Qualification	B.E. in Telecommunication	
<b>Signature of the Faculty</b>			

4	<b>Work experience:</b> Teaching Research Industry Others	1.5 years		
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		--		
		6 months in online tutor		
5	<b>Area of Specializations</b>	Microprocessors( 8085, 8086) communication		
6.a.	<b>Subjects teaching at Under Graduate Level:</b>	Microprocessor 8085, Advanced Microprocessor 8086		
		Microcontroller 8051, Digital Communication,		
		Analog Communication		
6.b	<b>Subjects teaching at Post Graduate Level:</b>	--		
		--		
		--		
		--		
7.a	<b>Research Guidance:</b>	--	--	
	<b>Masters'</b>	--	---	
	<b>Ph.D</b>	--	--	
7.b	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
		--	--	--
8	<b>Projects carried out</b>	Auto dialing for security systems		
9	<b>Patents</b>	--		
10	<b>Technology Transfers</b>	--		
11	<b>Research Publications</b>	--		
12	<b>No. of Books Published with details</b>	--		
13	<b>Strength</b>	Concentrating & understanding concepts Efforts for completing the task assigned		
	<b>Weakness</b>	Lack of practical knowledge Moderate communication skills, this needs improvement		

<b>1.a</b>	<b>Name</b>	Mr. Praveen K.B	
<b>1.b</b>	<b>Designation</b>	Lecturer	
<b>2</b>	<b>Date of Birth</b>	05.07.1984	
<b>3</b>	<b>Educational Qualification</b>	B.E	
<b>Signature of the Faculty</b>			
<b>4</b>	<b>Work experience:</b>		
	<b>Teaching</b>	1year	
	<b>Research</b>	--	
	<b>Industry</b>	--	
	<b>Others</b>	--	
<b>5</b>	<b>Area of Specializations</b>	Telecommunication	
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	ITC, Logic design, EMI	
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	--	
		--	
		--	
		--	
<b>7.a</b>	<b>Research Guidance:</b>	--	--
	<b>Masters'</b>	--	---
	<b>Ph.D</b>	--	--
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal
		--	--
			Conferences
			--
<b>8</b>	<b>Projects carried out</b>	Network traffic management Wireless audio video transmission using remote control	
<b>9</b>	<b>Patents</b>	--	
<b>10</b>	<b>Technology Transfers</b>	--	
<b>11</b>	<b>Research Publications</b>	--	
<b>12</b>	<b>No. of Books Published with details</b>	--	
<b>13</b>	<b>Strength</b>	--	
	<b>Weakness</b>		

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<b>1.a</b>	<b>Name</b>	Mrs. Anitha Suresh		
<b>1.b</b>	<b>Designation</b>	Lecturer		
<b>2</b>	<b>Date of Birth</b>	12.05.1979		
<b>3</b>	<b>Educational Qualification</b>	B.E		
<b>Signature of the Faculty</b>				
<b>4</b>	<b>Work experience:</b>			
	<b>Teaching</b>	6 months (PESIT)		
	<b>Research</b>	10 Months(WIPRO)		
	<b>Industry</b>	10 Months (WIPRO), INFONICH(2 yrs)		
	<b>Others</b>	--		
<b>5</b>	<b>Area of Specializations</b>	Worked in Industry in DSP		
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	Digital Communication		
		Digital Signal Processing		
		--		
<b>6.b</b>	<b>Subjects teaching at Post Graduaqte Level:</b>	--		
		--		
		--		
		--		
<b>7.a</b>	<b>Research Guidance:</b>	--		
	<b>Masters'</b>	--		
	<b>Ph.D</b>	--		
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
		--	--	--
<b>8</b>	<b>Projects carried out</b>	--		
<b>9</b>	<b>Patents</b>	--		
<b>10</b>	<b>Technology Transfers</b>	--		
<b>11</b>	<b>Research Publications</b>	--		
<b>12</b>	<b>No. of Books Published with details</b>	--		
<b>13</b>	<b>Strength</b>	1. DSP, Matlab 6.0, Simulink, VC++, Code Composer Studio 2.0 (TMS Kit), Excel, 2. MS Office, Power Point, Windows (98/2000/XP), Unix, C Language, 1. Lack of knowledge in networking.		
	<b>Weakness</b>			

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Bangalore – 560078

REF VII – INDIVIDUAL FACULTY PROFILE

<b>1.a</b>	<b>Name</b>	Mr. Jayaraj V.S		
<b>1.b</b>	<b>Designation</b>	Lecturer		
<b>2</b>	<b>Date of Birth</b>	25/07/1984		
<b>3</b>	<b>Educational Qualification</b>	M. Tech		
<b>Signature of the Faculty</b>				
<b>4</b>	<b>Work experience:</b>			
	<b>Teaching</b>	Nil		
	<b>Research</b>	Nil		
	<b>Industry</b>	Nil		
	<b>Others</b>	Nil		
<b>5</b>	<b>Area of Specializations</b>	Mobile Communication, DIP, Network Security		
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	--		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	--		
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		--		
<b>7.a</b>	<b>Research Guidance:</b>	--	--	
	<b>Masters'</b>	--	--	
	<b>Ph.D</b>	--	--	
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
		--	--	02
<b>8</b>	<b>Projects carried out</b>	Image denoising		
<b>9</b>	<b>Patents</b>	--		
<b>10</b>	<b>Technology Transfers</b>	--		
<b>11</b>	<b>Research Publications</b>	--		
<b>12</b>	<b>No. of Books Published with details</b>	--		
<b>13</b>	<b>Strength</b>			
	<b>Weakness</b>			


**DAYANANDA SAGAR COLLEGE OF ENGINEERING**

**Bangalore – 560078**

<b>1.a</b>	<b>Name</b>	Mr. Dhyanand .N	
<b>1.b</b>	<b>Designation</b>	Lecturer	
<b>2</b>	<b>Date of Birth</b>	1/03/1982	
<b>3</b>	<b>Educational Qualification</b>	M. Tech	
<b>Signature of the Faculty</b>			
<b>4</b>	<b>Work experience:</b>	--	
	<b>Teaching</b>	--	
	<b>Research</b>	--	
	<b>Industry</b>	--	
	<b>Others</b>	--	
<b>5</b>	<b>Area of Specializations</b>	Embedded Domain	
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	--	
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	--	
<b>7.a</b>	<b>Research Guidance:</b>	--	
	<b>Masters'</b>	--	
	<b>PhD</b>	--	
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal    International Journal    Conferences	
<b>8</b>	<b>Projects carried out</b>	--	
<b>9</b>	<b>Patents</b>	--	
<b>10</b>	<b>Technology Transfers</b>	--	
<b>11</b>	<b>Research Publications</b>	--	
<b>12</b>	<b>No. of Books Published with details</b>	--	
<b>13</b>	<b>Strength</b>	Honesty, punctuality	
	<b>Weakness</b>	Short tempered	

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**Bangalore – 560078**

<b>1.a</b>	<b>Name</b>	Mr. C. Jayanth		
<b>1.b</b>	<b>Designation</b>	Lecturer		
<b>2</b>	<b>Date of Birth</b>	11/12/1980		
<b>3</b>	<b>Educational Qualification</b>	M. Tech		
<b>Signature of the Faculty</b>				
<b>4</b>	<b>Work experience:</b>			
	Teaching	--		
	Research	--		
	Industry	--		
	Others	--		
<b>5</b>	<b>Area of Specializations</b>	Digital Electronics & Communication Systems		
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	--		
		--		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	--		
		--		
<b>7.a</b>	<b>Research Guidance:</b>	--	--	--
	Masters'	--	--	--
	PhD	--	--	--
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
<b>8</b>	<b>Projects carried out</b>	3, 2 in B.E. and 1 in M.Tech		
<b>9</b>	<b>Patents</b>			
<b>10</b>	<b>Technology Transfers</b>			
<b>11</b>	<b>Research Publications</b>			
<b>12</b>	<b>No. of Books Published with details</b>			
<b>13</b>	<b>Strength</b>			
	<b>Weakness</b>			

## DAYANANDA SAGAR COLLEGE OF ENGINEERING


Bangalore – 560078


REF VII – INDIVIDUAL FACULTY PROFILE

<b>1.a</b>	<b>Name</b>	Mr. Shree Prasad		
<b>1.b</b>	<b>Designation</b>	Lecturer		
<b>2</b>	<b>Date of Birth</b>	09.11.1984		
<b>3</b>	<b>Educational Qualification</b>	M. Tech		
<b>Signature of the Faculty</b>				
<b>4</b>	<b>Work experience:</b>	<b>Teaching</b>	--	
		<b>Research</b>	--	
		<b>Industry</b>	--	
		<b>Others</b>	--	
<b>5</b>	<b>Area of Specializations</b>	Communication, Signal Processing		
<b>6.a.</b>	<b>Subjects teaching at Under Graduate Level:</b>	Digital Signal Processing		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	--		
		--		
		--		
		--		
<b>7.a</b>	<b>Research Guidance:</b>	--	--	
	<b>Masters'</b>	--	--	
	<b>Ph.D</b>	--	--	
<b>7.b</b>	<b>No. of papers published in (over past three years)</b>	National Journal	International Journal	Conferences
		1	--	1
<b>8</b>	<b>Projects carried out</b>	At RVCE		
<b>9</b>	<b>Patents</b>	--		
<b>10</b>	<b>Technology Transfers</b>	--		
<b>11</b>	<b>Research Publications</b>	02		
<b>12</b>	<b>No. of Books Published with details</b>	--		
<b>13</b>	<b>Strength</b>			
	<b>Weakness</b>			

<b>1.a</b>	<b>Name</b>	Basavaraj S M		
<b>1.b</b>	<b>Designation</b>	Lecturer		
<b>2</b>	<b>Date of Birth</b>	9-6-82		
<b>3</b>	<b>Educational Qualification</b>	M.Tech – VLSI & Embedded Systems		
<b>4</b>	<b>Work Experience:</b>			
	<b>Teaching</b>	-		
	<b>Research</b>	-		
	<b>Industry</b>	-		
	<b>Others</b>	-		
<b>5</b>	<b>Area of Specializations</b>	VLSI & Embedded Systems		
<b>6.a</b>	<b>Subjects teaching at Under Graduate Level:</b>	Basic Electronics		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	-		
<b>7.a</b>	<b>Research Guidance:</b>	<b>Particulars</b>	<b>Number</b>	
	<b>Masters'</b>	-	-	
	<b>Ph. D</b>	-	-	
<b>7. b</b>	<b>No. of papers published in (over past three years)</b>	<b>National Journal</b>	<b>International Journal</b>	<b>Conferences</b>
		-	-	-
<b>8</b>	<b>Projects carried out</b>	-		
<b>9</b>	<b>Patents</b>	-		
<b>10</b>	<b>Technology Transfers</b>	-		
<b>11</b>	<b>Research Publications</b>	-		
<b>12</b>	<b>No. of Books Published with details</b>	-		
<b>13</b>	<b>Strength</b>	Positive Thinking		
	<b>Weakness</b>	Slow Starter		

<b>1.a</b>	<b>Name</b>	Santhosh B		
<b>1.b</b>	<b>Designation</b>	Lecturer		
<b>2</b>	<b>Date of Birth</b>	3-3-1982		
<b>3</b>	<b>Educational Qualification</b>	M.Tech. (Electronics & Electrical Communication)		
<b>4</b>	<b>Work Experience:</b>			
	<b>Teaching</b>	-		
	<b>Research</b>	-		
	<b>Industry</b>	-		
	<b>Others</b>	-		
<b>5</b>	<b>Area of Specializations</b>	Electronics & Electrical Communication		
<b>6.a</b>	<b>Subjects teaching at Under Graduate Level:</b>	Basic Electronics		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	-		
<b>7.a</b>	<b>Research Guidance:</b>	<b>Particulars</b>	<b>Number</b>	
	<b>Masters'</b>	-	-	
	<b>Ph. D</b>	-	-	
<b>7. b</b>	<b>No. of papers published in (over past three years)</b>	<b>National Journal</b>	<b>International Journal</b>	<b>Conferences</b>
		-	-	-
<b>8</b>	<b>Projects carried out</b>	-		
<b>9</b>	<b>Patents</b>	-		
<b>10</b>	<b>Technology Transfers</b>	-		
<b>11</b>	<b>Research Publications</b>	-		
<b>12</b>	<b>No. of Books Published with details</b>	-		
<b>13</b>	<b>Strength</b>	Positive Thinking		
	<b>Weakness</b>	-		

<b>1.a</b>	<b>Name</b>	DIVYA T.		
<b>1.b</b>	<b>Designation</b>	Lecturer		
<b>2</b>	<b>Date of Birth</b>	22 – 08 – 1984		
<b>3</b>	<b>Educational Qualification</b>	B.E. (Electronics & Comm.), M.Tech. (VLSI Design & Embedded System)		
				
<b>4</b>	<b>Work Experience:</b>			
	<b>Teaching</b>	-		
	<b>Research</b>	-		
	<b>Industry</b>	-		
	<b>Others</b>	-		
<b>5</b>	<b>Area of Specializations</b>	VLSI Design & Embedded System		
<b>6.a</b>	<b>Subjects teaching at Under Graduate Level:</b>	-		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	-		
<b>7.a</b>	<b>Research Guidance:</b>	<b>Particulars</b>	<b>Number</b>	
	<b>Masters'</b>	-	-	
	<b>Ph. D</b>	-	-	
<b>7. b</b>	<b>No. of papers published in (over past three years)</b>	<b>National Journal</b>	<b>International Journal</b>	<b>Conferences</b>
		-	-	-
<b>8</b>	<b>Projects carried out</b>	-		
<b>9</b>	<b>Patents</b>	-		
<b>10</b>	<b>Technology Transfers</b>	-		
<b>11</b>	<b>Research Publications</b>			
<b>12</b>	<b>No. of Books Published with details</b>	-		
<b>13</b>	<b>Strength</b>	Keen to learn new subjects		
	<b>Weakness</b>			

<b>1.a</b>	<b>Name</b>	PRADEEP M.		
<b>1.b</b>	<b>Designation</b>	Lecturer		
<b>2</b>	<b>Date of Birth</b>	22 – 06 – 1982		
<b>3</b>	<b>Educational Qualification</b>	B.E. (Electronics & Comm.), M.Tech. (Industrial Electronics)		
				
<b>4</b>	<b>Work Experience:</b>			
	<b>Teaching</b>	-		
	<b>Research</b>	-		
	<b>Industry</b>	“Software Engineer” in Microcore Solutions Pvt. Ltd., for 2 yrs.		
	<b>Others</b>	-		
<b>5</b>	<b>Area of Specializations</b>	Industrial Electronics – Software Testing, Networking		
<b>6.a</b>	<b>Subjects teaching at Under Graduate Level:</b>	Basic Electronics		
<b>6.b</b>	<b>Subjects teaching at Post Graduate Level:</b>	-		
<b>7.a</b>	<b>Research Guidance:</b>	<b>Particulars</b>	<b>Number</b>	
	<b>Masters’</b>	Dr. M.N. Shanmukha Swamy	-	
	<b>Ph. D</b>	-	-	
<b>7. b</b>	<b>No. of papers published in (over past three years)</b>	<b>National Journal</b>	<b>International Journal</b>	<b>Conferences</b>
		-	-	-
<b>8</b>	<b>Projects carried out</b>	-		
<b>9</b>	<b>Patents</b>	-		
<b>10</b>	<b>Technology Transfers</b>	-		
<b>11</b>	<b>Research Publications</b>	-		
<b>12</b>	<b>No. of Books Published with details</b>	..		
<b>13</b>	<b>Strength</b>	Positive Thinking		
	<b>Weakness</b>	Slow Starter		